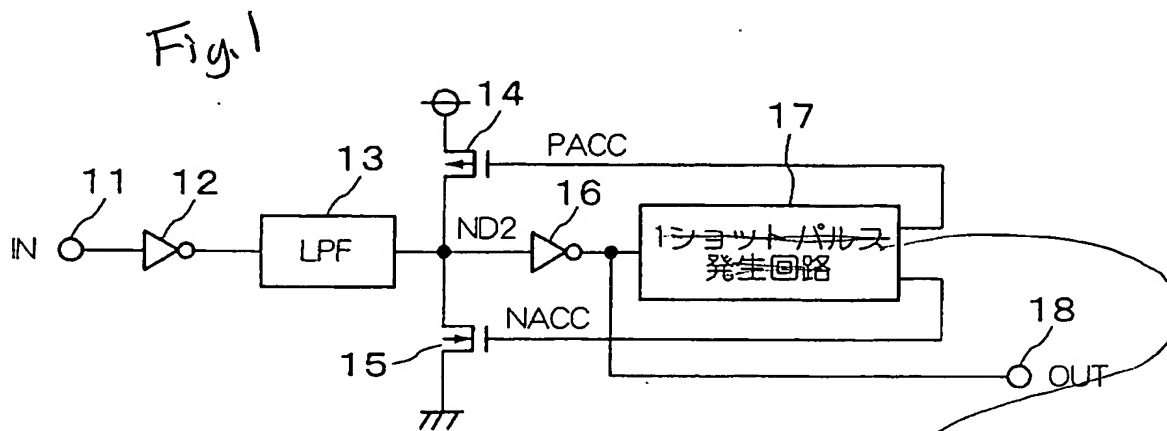


【書類名】 図面

【図1】



One-Shot Pulse Generating Circuit

Fig. 2A

IN

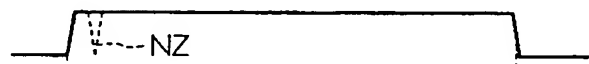


Fig. 2B

ND2



Fig. 2C

OUT

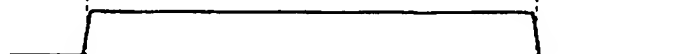


Fig. 2D

PACC

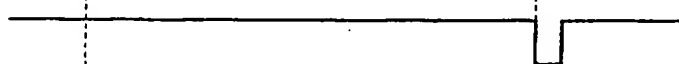
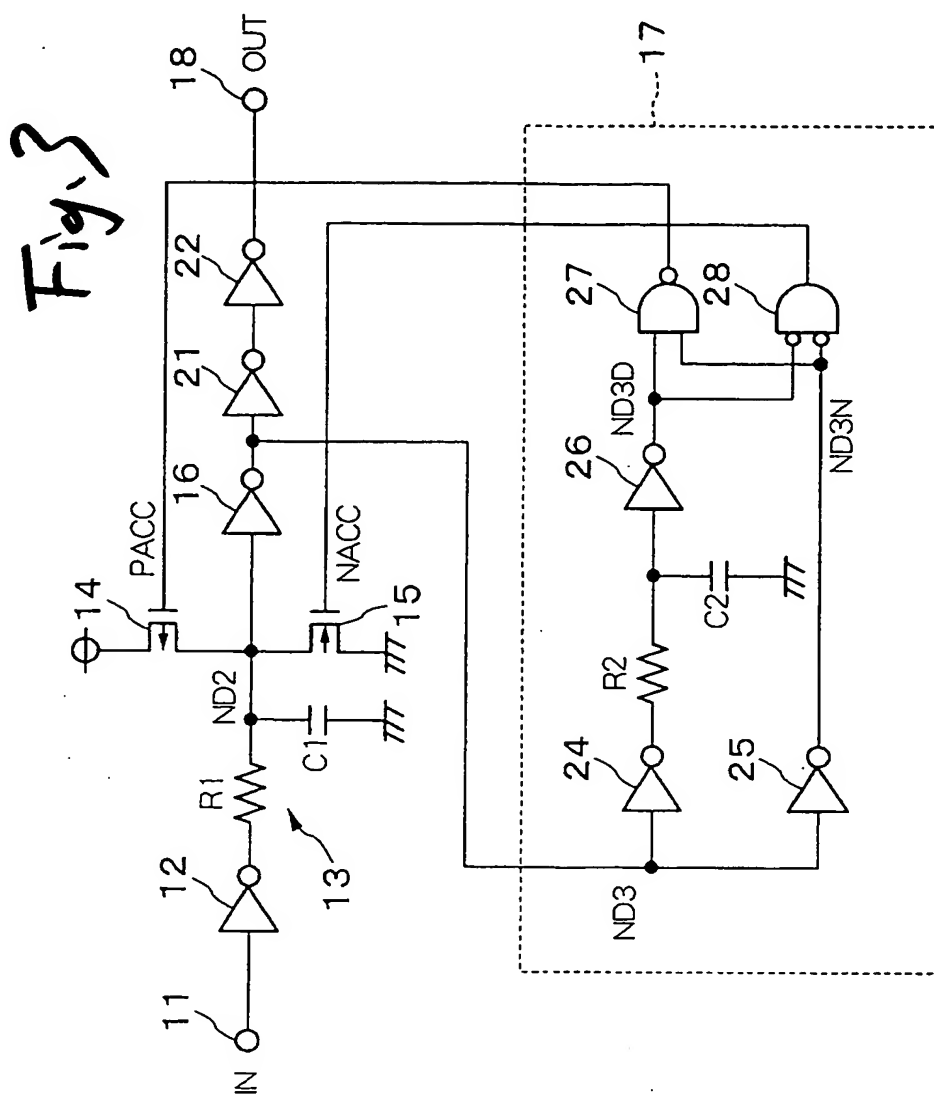


Fig. 2E

NACC



~~【圖3】~~



~~図4~~

Fig. 4A

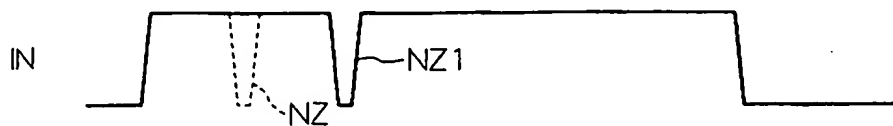


Fig. 4B



Fig. 4C

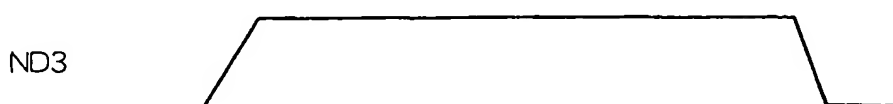


Fig. 4D



Fig. 4E

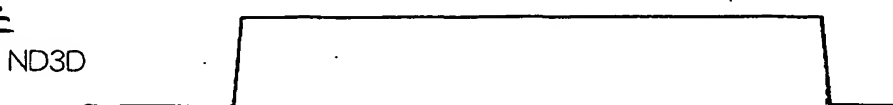


Fig. 4F



Fig. 4G

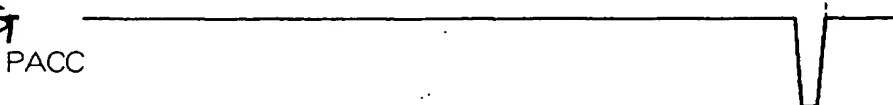


Fig. 4H

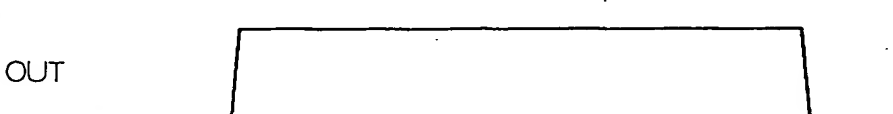


Fig. 5

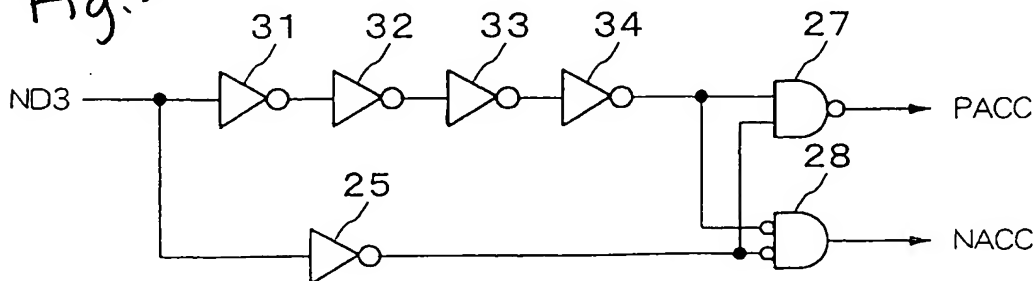


図6
Fig. 6

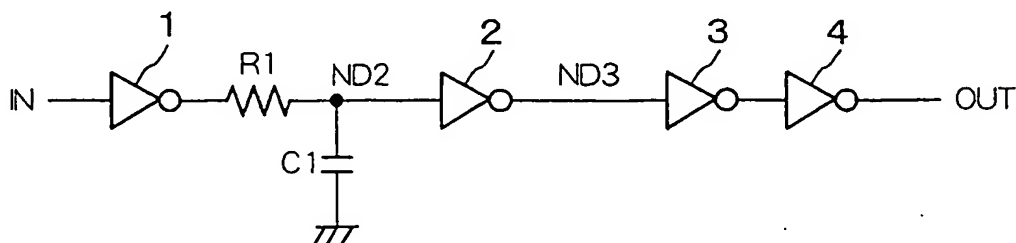


Fig. 7A

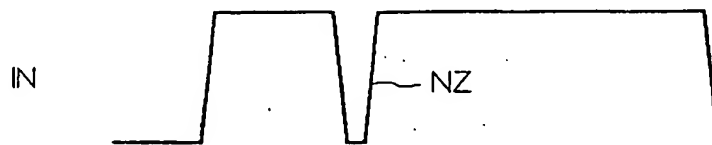


Fig. 7B

ND2

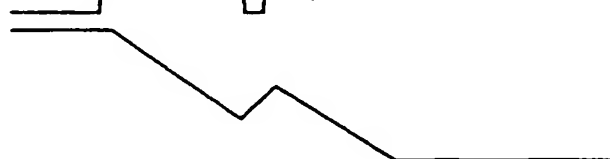


Fig. 7C

ND3

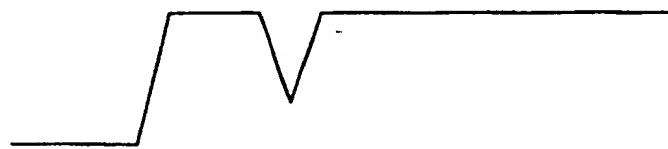


Fig. 7D

OUT



{図8}

Fig. 8A



Fig. 8B

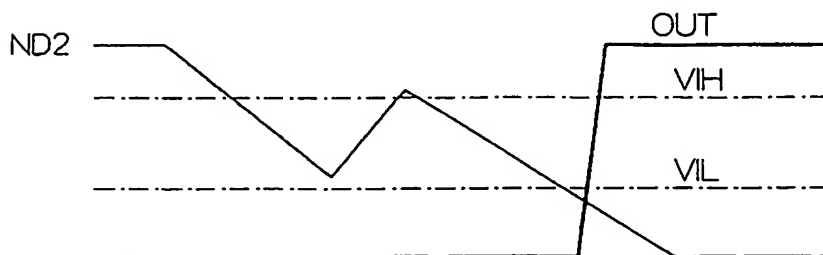


Fig. 8C

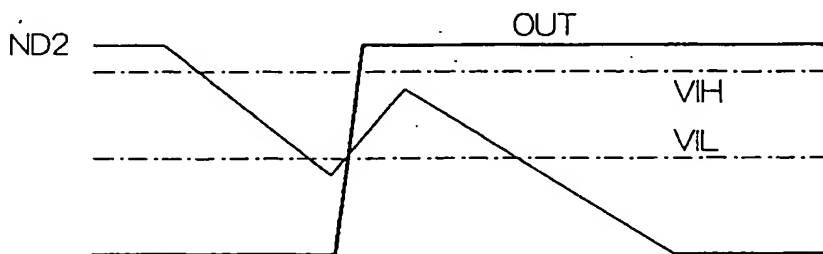


Fig. 8D

